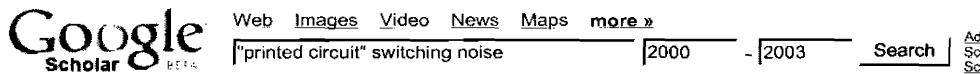


EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	35	703/18.cccls. and @pd>="20071001"	US-PGPUB; USPAT; EPO; DERWENT	OR	OFF	2008/07/21 15:05
L2	6	("5745371" "6557154" "6571184" "6618834" "6631502" "6850878"). PN.	US-PGPUB; USPAT; USOCR	OR	OFF	2008/07/21 15:09
L3	97	(noise near analysis) and (printed near circuit) and @ad<"20030601"	US-PGPUB; USPAT; EPO; DERWENT	OR	OFF	2008/07/21 15:12



Scholar All articles - Recent articles Results 1 - 10 of about 4,890 for **"printed circuit" switching noise**

A novel power plane with integrated simultaneous switching noise mitigation capability using high ... - all 9 versions »

T Kamgaing, OM Ramahi - Microwave and Wireless Components Letters, IEEE [see also ..., 2003 - ieeexplore.ieee.org

... switching of all these gates approaches the noise tolerance of static CMOS circuits, a degradation of the signal integrity can occur. In printed circuit boards ...

Cited by 64 - Related Articles - Web Search - BL Direct

Modeling of simultaneous switching noise in high speed systems - all 5 versions »

S Chun, M Swaminathan, LD Smith, J Srinivasan, Z ... - Advanced Packaging, IEEE Transactions on [see also ..., 2001 - ieeexplore.ieee.org

... CHUN et al.: MODELING OF SIMULTANEOUS SWITCHING NOISE IN HIGH SPEED SYSTEMS ... been used to model a system containing packages on a printed circuit board, demon ...

Cited by 63 - Related Articles - Web Search - BL Direct

... -domain (FDFD) algorithm for analysis of switching noise in printed circuit boards and packages - all 11 versions »

OM Ramahi, V Subramanian, B Archambeault - Advanced Packaging, IEEE Transactions on [see also ..., 2003 - ieeexplore.ieee.org

... Abstract—Simultaneous switching noise (SSN) compromises the integrity of the power distribution structure on multilayer printed circuit boards (PCB). ...

Cited by 8 - Related Articles - Web Search - BL Direct

Estimating the noise mitigation effect of local decoupling in printed circuit boards - all 7 versions »

J Fan, W Cui, JL Drewniak, TP Van Doren, JL ... - Advanced Packaging, IEEE Transactions on [see also ..., 2002 - ieeexplore.ieee.org

... I. INTRODUCTION H IGH-FREQUENCY noise in printed circuit boards (PCBs) results from both simultaneous switching of digital logic within the core, as well as ...

Cited by 14 - Related Articles - Web Search - BL Direct

... bandgap structures for suppression and isolation of the parallel-plate noise in high-speed circuits - all 6 versions »

R Abhari, GV Eleftheriades - Microwave Theory and Techniques, IEEE Transactions on, 2003 - ieeexplore.ieee.org

... vias in parallel-plate environments [eg, multilayer printed circuit boards (PCBs) ... as power/ground noise, ground bounce, simultaneous switching noise (SSN), etc. ...

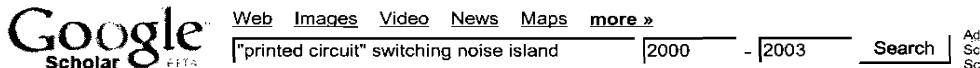
Cited by 83 - Related Articles - Web Search - BL Direct

Power-bus decoupling with embedded capacitance in printed circuit board design - all 4 versions »

M Xu, TH Hubing, J Chen, TP Van Doren, JL Drewniak ... - Electromagnetic Compatibility, IEEE Transactions on, 2003 - ieeexplore.ieee.org

... for reducing power-bus noise in high-speed printed circuit board designs. ... bus noise (delta-I noise, ground bounce noise, simultaneous switch noise), power plane ...

Cited by 32 - Related Articles - Web Search - BL Direct



Scholar All articles - Recent articles Results 1 - 10 of about 145 for **"printed circuit" switching noise island**

A novel power plane with integrated simultaneous switching noise mitigation capability using high ... - all 9 versions »

T Kamgaing, OM Ramahi - Microwave and Wireless Components Letters, IEEE [see also ...], 2003 - ieeexplore.ieee.org

... In **printed circuit** boards (PCB), each digital gate ... source and connecting the **island** (isolated active ... The trench prevents the **switching noise** from propagating ...

Cited by 64 - Related Articles - Web Search - BL Direct

Power bus isolation using power islands in **printed circuit** boards - all 2 versions »

J Chen, TH Hubing, TP Van Doren, RE DuBroff - Electromagnetic Compatibility, IEEE Transactions on, 2002 - ieeexplore.ieee.org

... I. INTRODUCTION Power bus **noise** caused by the **switching** of integrated ... is often desirable to isolate relatively noisy regions of a **printed** circuit board (PCB ...

Cited by 8 - Related Articles - Web Search - BL Direct

DC Power-Bus Noise Isolation With Power-Plane Segmentation - all 3 versions »

W Cui, J Fan, Y Ren, H Shi, JL Drewniak, RE ... - IEEE TRANSACTIONS ON ELECTROMAGNETIC COMPATIBILITY, 2003 - ieeexplore.ieee.org

... bus **noise** isolation in multilayer **printed circuit** board (PCB ... an effective method for minimizing **noise** propagation ... provide power to some fast **switching** or noisy ...

Cited by 21 - Related Articles - Web Search - BL Direct

DC power bus **noise** isolation with power islands

W Cui, J Fan, H Shi, JL Drewniak - Electromagnetic Compatibility, 2001. EMC. 2001 IEEE ... 2001 - ieeexplore.ieee.org

... power planes are often used for DC power bus **noise** isolation in multi-layered **printed circuit** board (PCB ... power bus **noise** results from **switching** of IC ...

Cited by 5 - Related Articles - Web Search

Modeling and Analysis of Power Distribution Networks for Gigabit Applications - all 5 versions »

J Choi, SH Min, JH Kim, M Swaminathan, WW Beyene, ... - 2003 - doi.ieeecomputersociety.org

... simultaneously **switching** circuits in a **printed circuit** board (PCB ... thousands of drivers that **switch** simultaneously within ... filter any power supply **noise**, as shown ...

Cited by 21 - Related Articles - Web Search - BL Direct

Electromagnetic radiation and simultaneous switching noise in aCMOS device packaging

T Sudo, Y Ko, S Sakaguchi, T Tokumaru - Electronic Components and Technology Conference, 2000. 2000 ..., 2000 - ieeexplore.ieee.org

... is found that the powerground **island** with decoupling ... and Simulation of Simultaneous **Switching Noise**", IEEE Trans ... in a digital **printed circuit** board", IEEE ...

Cited by 5 - Related Articles - Web Search - BL Direct

Modeling DC power-bus structures with vertical discontinuities using a circuit extraction approach ... - all 4 versions »

J Fan, H Shi, A Orlandi, JL Knighten, JL Drewniak - Advanced Packaging, IEEE Transactions on [see also ...], 2001 - ieeexplore.ieee.org

... Finally, the **printed circuit** board layer stackup must be ... the PCB to mitigate



[Web](#) [Images](#) [Video](#) [News](#) [Maps](#) [more »](#)

"printed circuit" switching noise island peec [2000 - 2003]

[Search](#)

Ad

Sc

Sc

Scholar

Results 1 - 5 of 5 for "printed circuit" switching noise island peec. (0.25 seconds)

Tip: Try removing quotes from your search to get more results.

DC power bus noise isolation with power islands

W Cui, J Fan, H Shi, JL Drewniak - Electromagnetic Compatibility, 2001. EMC. 2001 IEEE ..., 2001 - ieeexplore.ieee.org

... the connection of the power **island** and other power ... and J. L. Prince, Simultaneous **Switching Noise** of CMOS ... H. Shi, "Study of **printed circuit** board power bus de ...

Cited by 5 - Related Articles - Web Search

DC Power-Bus Noise Isolation With Power-Plane Segmentation - all 3 versions »

W Cui, J Fan, Y Ren, H Shi, JL Drewniak, RE ... - IEEE TRANSACTIONS ON ELECTROMAGNETIC COMPATIBILITY, 2003 - ieeexplore.ieee.org

... bus **noise** isolation in multilayer **printed circuit** board (PCB ... as indicated in the previous power **island** case. ... and JL Prince, Simultaneous **Switching Noise** of CMOS ...

Cited by 21 - Related Articles - Web Search - BL Direct

Modeling DC power-bus structures with vertical discontinuities using a circuit extraction approach ... - all 4 versions »

J Fan, H Shi, A Orlandi, JL Knighten, JL Drewniak - Advanced Packaging, IEEE Transactions on [see also ..., 2001 - ieeexplore.ieee.org

... Finally, the **printed circuit** board layer stackup must be ... the PCB to mitigate simultaneous **switching noise** is an ... the criterion of determining **noise** is necessary ...

Cited by 12 - Related Articles - Web Search - BL Direct

Progress in the methodologies for the electrical modeling of interconnects and electronic packages - all 3 versions »

AE Ruehli, AC Cangellaris - Proceedings of the IEEE, 2001 - ieeexplore.ieee.org

... distribution circuit in multilayered **printed circuit** boards, to ... delay, crosstalk, and simultaneous **switching noise** can be performed using reduced **PEEC** models ...

Cited by 54 - Related Articles - Web Search - BL Direct

[book] Pcb Design for Real-World Emi Control

B Archambeault - 2002 - books.google.com

... Introduction to EMI/EMC Design for **Printed Circuit Boards** / 3 ... energy can induce d/dt **noise** in circuits, causing false logic **switching** and improper ...

Cited by 14 - Related Articles - Web Search - BL Direct

Key authors: [J Fan](#) - [W Cui](#) - [H Shi](#) - [J Drewniak](#) - [Y Ren](#)

"printed circuit" switching noise island [Search](#)

[Google Home](#) - [About Google](#) - [About Google Scholar](#)

©2008 Google



Web Images Video News Maps more »

"printed circuit" switching noise island ppec 1990 - 2003

Search

Ad
Sc
Sc

Scholar

Results 1 - 5 of 5 for "printed circuit" switching noise island ppec. (0.39 seconds)

Tip: Try removing quotes from your search to get more results.

DC power bus noise isolation with power islands

W Cui, J Fan, H Shi, JL Drewniak - Electromagnetic Compatibility, 2001. EMC. 2001 IEEE ..., 2001 - ieeexplore.ieee.org

... the connection of the power **island** and other power ... and J. L. Prince, Simultaneous **Switching Noise** of CMOS ... H. Shi, "Study of **printed circuit** board power bus de ...

Cited by 5 - Related Articles - Web Search

DC Power-Bus Noise Isolation With Power-Plane Segmentation - all 3 versions »

W Cui, J Fan, Y Ren, H Shi, JL Drewniak, RE ... - IEEE TRANSACTIONS ON ELECTROMAGNETIC COMPATIBILITY, 2003 - ieeexplore.ieee.org

... bus **noise** isolation in multilayer **printed circuit** board (PCB ... as indicated in the previous power **island** case. ... and JL Prince, Simultaneous **Switching Noise** of CMOS ...

Cited by 21 - Related Articles - Web Search - BL Direct

Modeling DC power-bus structures with vertical discontinuities using a circuit extraction approach ... - all 4 versions »

J Fan, H Shi, A Orlandi, JL Knighten, JL Drewniak - Advanced Packaging, IEEE Transactions on [see also ..., 2001 - ieeexplore.ieee.org

... Finally, the **printed circuit** board layer stackup must be ... the PCB to mitigate simultaneous **switching noise** is an ... the criterion of determining **noise** is necessary ...

Cited by 12 - Related Articles - Web Search - BL Direct

Progress in the methodologies for the electrical modeling of interconnects and electronic packages - all 3 versions »

AE Ruehli, AC Cangellaris - Proceedings of the IEEE, 2001 - ieeexplore.ieee.org

... distribution circuit in multilayered **printed circuit** boards, to ... delay, crosstalk, and simultaneous **switching noise** can be performed using reduced PEEC models ...

Cited by 54 - Related Articles - Web Search - BL Direct

book] Pcb Design for Real-World Emi Control

B Archambeault - 2002 - books.google.com

... Introduction to EMI/EMC Design for **Printed Circuit Boards** / 3 ... energy can induce $\frac{di}{dt}$ **noise** in circuits, causing false logic **switching** and improper ...

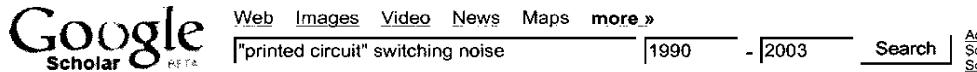
Cited by 14 - Related Articles - Web Search - BL Direct

Key authors: [J Fan](#) - [W Cui](#) - [H Shi](#) - [J Drewniak](#) - [Y Ren](#)

"printed circuit" switching noise islan [Search](#)

[Google Home](#) - [About Google](#) - [About Google Scholar](#)

©2008 Google

**Scholar All articles - Recent articles** Results 1 - 10 of about 11,300 for **"printed circuit" switching**

Computation of switching noise in printed circuit boards - all 2 versions »
JG Yook, V Chandramouli, LPB Katehi, KA Sakallah, ... - Components, Packaging, and Manufacturing Technology, Part A, ..., 1997 - ieeexplore.ieee.org
... Terms—Circuit simulation, decoupling capacitors, elec- tromagnetic modeling, ground bounce, **printed circuit board**, tiling, simultaneous **switching noise**. ...
Cited by 31 - Related Articles - Web Search - BL Direct

Power bus decoupling on multilayer printed circuit boards - all 2 versions »
TH Hubing, JL Drewniak, TP Van Doren, DM Hockanson - Electromagnetic Compatibility, IEEE Transactions on, 1995 - ieeexplore.ieee.org
... technique to the analysis of delta-I **noise** on integrated ... ground planes of the **printed circuit board**. ... When a **switching** device draws additional current from the ...
Cited by 87 - Related Articles - Web Search - BL Direct

A novel power plane with integrated simultaneous switching noise mitigation capability using high ... - all 9 versions »
T Kamgaing, OM Ramahi - Microwave and Wireless Components Letters, IEEE [see also ..., 2003 - ieeexplore.ieee.org
... **switching** of all these gates approaches the **noise** tolerance of static CMOS circuits, a degradation of the signal integrity can occur. In **printed circuit boards** ...
Cited by 64 - Related Articles - Web Search - BL Direct

Reducing simultaneous switching noise and EMI on ground/powerplanes by dissipative edge termination - all 7 versions »
I Novak, SM Inc, MA Burlington - Advanced Packaging, IEEE Transactions on [see also ..., 1999 - ieeexplore.ieee.org
... [17] P. Wetmore, F. Chua, and X. Yuan, "Modeling simultaneous **switching noise** in high speed multilayer packages and **printed circuit boards**," in Proc. ...
Cited by 56 - Related Articles - Web Search - BL Direct

Simultaneous switch noise and power plane bounce for CMOS technology - all 5 versions »
L Smith, SM Inc, P Alto - Electrical Performance of Electronic Packaging, 1999, 1999 - ieeexplore.ieee.org
... **circuit board** (PCB). ... of simulta- neously **switching** drivers inside an electronic package mounted on a PCB. The simu- ... **SSN noise** is due to power plane bounce. The ...
Cited by 39 - Related Articles - Web Search

Modeling of simultaneous switching noise in high speed systems - all 5 versions »
S Chun, M Swaminathan, LD Smith, J Srinivasan, Z ... - Advanced Packaging, IEEE Transactions on [see also ..., 2001 - ieeexplore.ieee.org
... CHUN et al.: MODELING OF SIMULTANEOUS **SWITCHING NOISE** IN HIGH SPEED SYSTEMS ... been used to model a system containing packages on a **printed circuit board**, demon ...
Cited by 63 - Related Articles - Web Search - BL Direct

Interconnect and circuit modeling techniques for full-chip powersupply noise analysis - all 2 versions »
HH Chen, JS Neely, IBMTJWR Center, Y Heights - Components, Packaging, and Manufacturing Technology, Part B: ..., 1998 - ieeexplore.ieee.org
... and achieve the desired resolution for a full-chip **switching noise** analysis, we